

Integration of Software Defined Radio and Add-on Board into Digital Communication Education with Hands-on Applications

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Abstract: Owing to the theory depend heavily on mathematical models in communication lessons, instructors find students lost in all the equations and notations that given. Setting a lab component for students to put together the theory with practical implementations is not very easy in many times because it may cost a large number of equipment that is not low-priced. Yet there are still some solutions which reduce the cost in designing a real-life communication laboratory and effective in teaching. We present the usage of NI-USRP Software Defined Radio (SDR) and Emona DATEX Add-on Board on digital modulation techniques that take part in communication engineering education. Most commonly used digital modulation techniques; Amplitude Shift Keying (ASK), Phase Shift Keying (PSK) and Frequency Shift Keying (FSK) are analyzed in both devices. Both modulations and demodulations of mentioned techniques are done by experimentally.

Keywords: Software Defined Radio, Communication Engineering Education, Digital Modulation Techniques, USRP, DATEX.

1. Introduction

In general most of the students at communication engineering start learning communication systems with the theory and the principles. However learning the theory and principles solely does not help students to design communication systems properly. They need to acquire experimental skills beside the theoretical domain. In order to bridge this gap instructors use several equipments such as function generators, oscilloscopes, spectrum analyzers that is not affordable many times.

With the development of microprocessors and software technology, traditional equipments began to transform into computer hosted devices which are low-cost, flexible and multi-functional. As an example, SDR, offers large number of tools to teach signals and communications [1]. Limits of traditional equipments, students being remained at the level of pre-digital area and the motivation for SDR based communication education is stated clearly in [2]. SDR refers that analog-to-digital (A/D) conversion is performed at the antenna connector, with all following signal processing (upconversion and/or downconversion, intermediate-frequency (IF) filtering, demodulation, etc.) performed in reconfigurable software using digital signal processing (DSP) techniques [3].

There are several examples of SDR based education courses. Domain analyses such as time, frequency, joint time-frequency, code, and modulation discussed in [4]. M-ary PSK modulation and demodulation implemented in [5]. AM implementation presented in [6]. Five week communication applications programme discussed in [7]. Therefore, multi-purpose usage of SDR in communication lessons will be educatory and preliminary

for real world.

In this study's first part we use NI-USRP 2922 supporting frequency range of 400 MHz to 4.4 GHz with SBX daughterboard. It's omni-directional vertical antenna provides 3 dBi gain for two-way high bandwidth communication. A Gigabit Ethernet connectivity provide data streaming to host processors enables simultaneously sending up to 50 MHz RF signal in and out of the USRP device [8].

Another way of using computer hosted device in communication education is Emona DATEX. By using it students can find the opportunity to experiment the real world signals in lessons.

The Emona DATEX, Digital Analog Telecommunications Experimenter, is an add-on which could serve as a basic training board in various concepts of communication lessons [9]. This add-on is fully integrated with the NI ELVIS II platform and is controllable through the NI LabVIEW development environment. All DATEX knobs and switches are manually controllable or programmable from LabVIEW.

The NI ELVIS II is an all-in-one electronics workstation that performs the functions of instruments which are conventionally found in an electrical engineering laboratory. The NI ELVIS II holds a removable prototyping board on which developers can build electrical circuits which connect the different ELVIS's instruments to achieve a particular experimental goal.

In communication education, examples of DATEX usage limited. Practical aspects of Direct Sequence Spread Spectrum (DSSS) communication discussed in [10]. AM/FM/BPSK signal generating experiments over the internet is presented in [11]. Time and frequency domain demonstrations discussed in [12].

2. ASK Modulation Applications

ASK is a form of amplitude modulation that the amplitude of the carrier wave is shifted proportionally to the amplitude of the digital signal. For a binary message sequence there are two levels; a binary "0" is one amplitude and a binary "1" is another amplitude. We can formulate ASK as;

$$s(t) = A m(t) \cos(2\pi fct) \quad 0 \leq t \leq T \quad (1)$$

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where A is a constant, $m(t) = 1$ or 0 , f_c is the carrier frequency, and T is the bit duration.

2.1. ASK Implementation with USRP

USRP Hardware Driver works on a variety of programming languages, operating systems, and development environments such as Linux, Windows, Mac and can be used with numerous frameworks such as GNU Radio, LabVIEW, Simulink, OpenBTS, OSSIE, Redhawk etc. In this study all applications done with LabVIEW because USRP devices that we have supported with licensed LabVIEW Framework.

The basic building block of LabVIEW is the virtual instrument (VI). Each VI consists of a block diagram and a front panel. The block diagram describes the functionality of the VI, while the front panel is a top level interface to the VI. Fig. 1 shows front panel of ASK application.

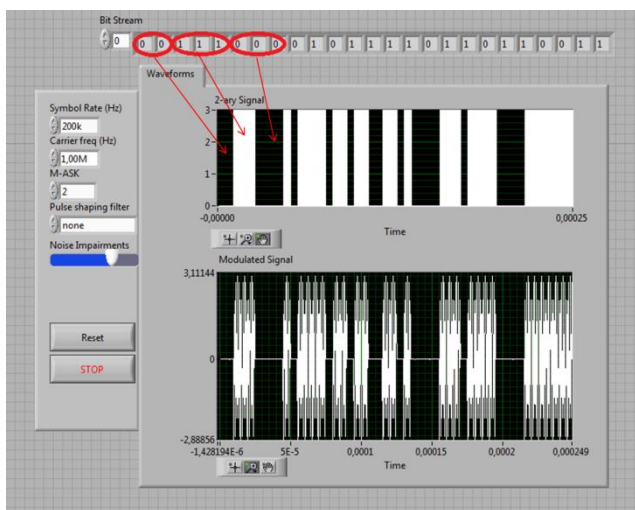


Figure 1. Front Panel of ASK Application.

At the top of the front panel bit stream represents digital data that is randomly produced. At the bottom of the front panel ASK signal is located. While generating ASK signal, all we have to do is to connect the subVIs to each other in an order and feed them with the wires properly. But the theories like Nyquist theorem is crucial while establishing symbol rate values via carrier frequency. Moreover for the getting pure ASK, sampling rate and noise impairment values must be chosen punctiliously. In Fig. 2 block diagram of the ASK application is shown.

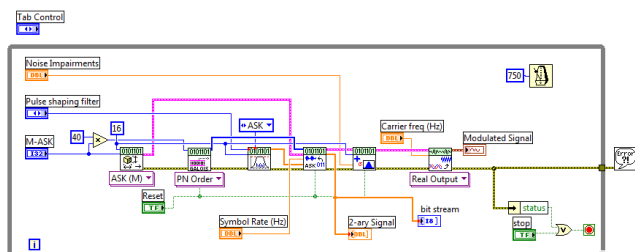


Figure 2. Block Diagram of ASK Application.

2.2. ASK Implementation with DATEx

In DATEx implementation we use Sequence Generator module for the digital data. 100 kHz master signal is quite appropriate for the carrier frequency because in ideal ASK communication system, we want the carrier frequency to be much higher than the bit rate of the digital data. Generating ASK signal is provided by

Dual Analog Switch module. In Fig. 3 modulation (Dotted) and demodulation (Lined) scheme of ASK is shown.

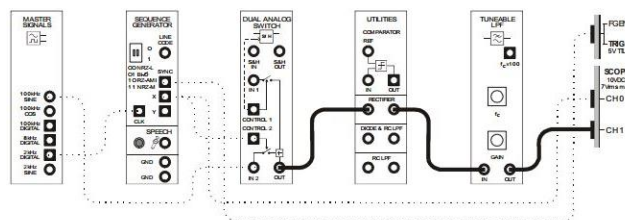


Figure 3. ASK Modulation and Demodulation Scheme.

In demodulation part of DATEx implementation for getting an envelope detector, rectifier on the Utilities module and the Tuneable Low Pass Filter (LPF) module is used. Experimental setup with DATEx is shown in Fig. 4.

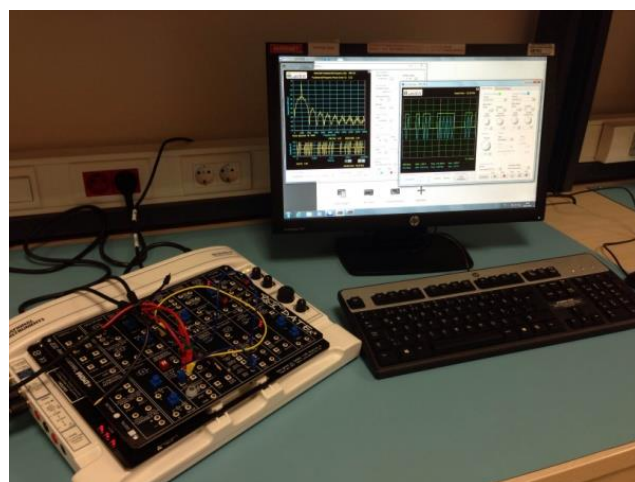


Figure 4. Experimental Setup with DATEx.

While selecting the carrier frequency there is not so many options in DATEx. In Master Signals module, signals that can be used in an implementation are; 100 kHz sine/cosine, 2 kHz sine, 2-8 kHz digital. On the contrary in USRP students can use all the frequencies supported by daughterboard (400 MHz-4.4.GHz).

3. FSK Modulation Applications

FSK refers to a type of frequency modulation that assigns bit values to discrete frequency levels. In other words frequency does not fluctuate over symbol time but fluctuates in discrete amounts in response to message. Amplitude remains unchanged. This is a desirable characteristic for improving the power efficiency of transmitters. In FSK modulated signal consist of two different carriers;

$$s1(t) = Ac \cos(2\pi f1t) \tag{2}$$

$$s2(t) = Ac \cos(2\pi f2t) \tag{3}$$

One of the signal above is in response to “0” and the other is in response to “1”.

3.1. FSK Implementation with USRP

LabVIEW interacts with the USRP transmitter by means of four basic functions located on the block diagram’s palette. Transmitter session starts with “Open Tx Session” and goes on

with “Configure Signal” used to set parameter values in the USRP. “Write Tx Data” writes the baseband signal to the USRP for transmission and your code should be before this function. Then “Close Session” terminates transmitter operation once the while loop ends. It has five basic functions at the receiver part similarly.

While generating FSK signal, if we rewrite the frequencies as a deviation from the carrier frequency then it is called frequency deviation. This is excursion of the signal above and below the carrier frequency and indicates the quality of signal as it has particular importance in relation to bandwidth.

We can define the bandwidth of a pulse shaping filter that we create in LabVIEW as;

$$BW = (1 + \alpha) * Rs + 2 * \Delta f \quad (4)$$

where R_s is symbol rate, Δf is frequency deviation and α is roll-off factor.

In the receiver part of the application beside the time and frequency domain you can use eye diagram demonstration as in Fig.5.

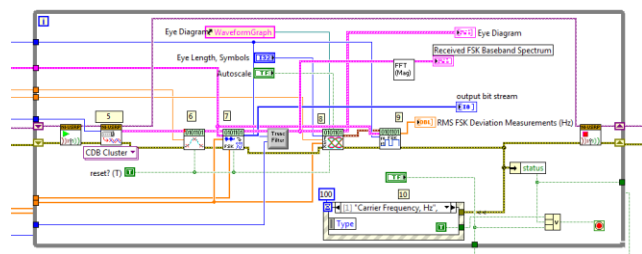


Figure 5. Block Diagram of FSK Receiver Application.

Depending on the value of modulation index distortion on the received signal can be seen on the eye diagram easily.

As in Fig. 6 by changing symbol rate, alpha, filter type etc. you can easily discover conversions in the modulated signal. After mathematical theory, showing their importance on a real life signal would be very beneficial for students.

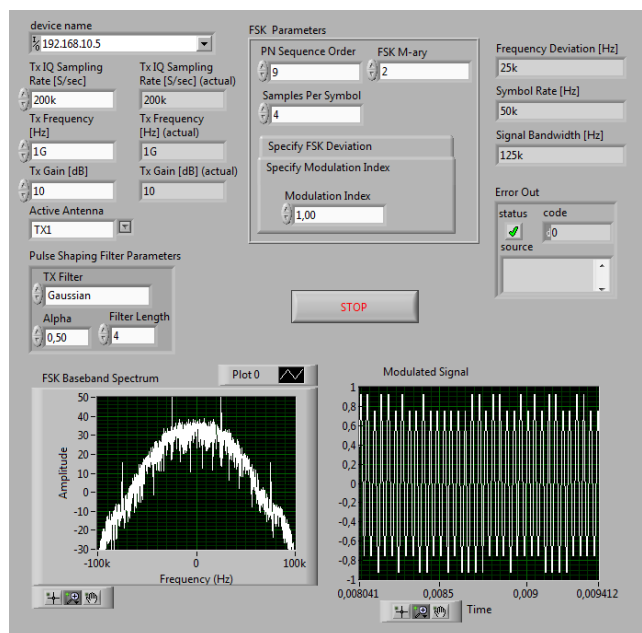


Figure 6. Front Panel of FSK Transmitter Application.

3.2. FSK Implementation with DATEx

For generating FSK signal in DATEx, Voltage Controlled

Oscillator (VCO) method is used. The Sequence Generator module is used for modeling the digital data and its SYNC output triggering the scope to provide a stable screen. By using a filter we can distinguish one of the sinewave in FSK and then demodulate it with the help of an envelope detector. For this purpose Tunable LPF module is used in recovering part and then DIODE and RC LPF on the Utilities module is used to form the envelope detector. As in Fig. 7 modulation (Dotted) and demodulation (Lined) scheme of FSK is shown.

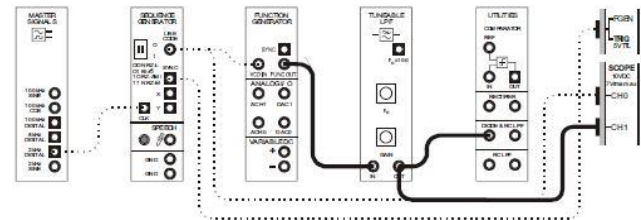


Figure 7. FSK Modulation and Demodulation Scheme.

Comparator is a useful function for restoring distorted signal. In demodulation part of the application if signal that we recover is not clear we can use comparator function in the Utilities module either.

As seen clearly from the block diagram, setting FSK demodulation is more difficult in LabVIEW than the one in DATEx. As we mentioned before some of the functions used in block diagram are standard while designing real signals. And their connections seem a little bit complicated. But it is obvious that USRP gives us chance to radiate our signal in the air and demodulating/recovering a signal with additive noise is not so simple.

4. PSK Modulation Applications

In PSK, information is encoded on the phase of the transmitted carrier that changes between two values according to the binary signal level. When encoding bits, the phase shift could be 0 degrees for encoding a "0," and 180 degrees for encoding a "1," or the phase shift could be -90 degrees for "0" and +90 degrees for a "1," thus making the representations for "0" and "1" a total of 180 degrees difference. PSK is ideal among FSK and ASK in providing the lowest average power needed for a given bit error rate. A binary PSK signal can be defined by;

$$s1(t) = A \cos 2\pi fct \quad 0 \leq t \leq T \quad (5)$$

$$s2(t) = -A \cos 2\pi fct \quad 0 \leq t \leq T \quad (6)$$

These two signals have the same frequency and energy but they have a correlation coefficient of -1.

4.1. PSK Implementation with USRP

Our laboratory has two USRP radios, one for transmission and the other one for reception. There are two possible configurations:

- Both radios connected to single computer (running both the transmitter and receiver VIs) with a dual-port Gigabit Ethernet interface, or using a data switch.

- Each radio connected to a different computer; one running the transmitter VI, and the other, the receiver VI.

We preferred the first one for our applications because it is better and faster for managing all the process in one screen. Our experimental set up with USRP can be seen on Fig. 9. Connection between the two USRP is provided with the Multiple Input

Multiple Output (MIMO) cable.



Figure 9. Experimental Setup with USRP.

Errors in communication design can be found easily using a constellation view. If the problem is related to phase, the constellation will spread around the circle. If the problem is related to magnitude, the constellation will spread off the circle, either towards or away from the origin. These graphical techniques assist in isolating problems much faster than when using other techniques. With the help of Modulation Toolkit Examples in the LabVIEW library, PSK transmitter application can be implemented easily as shown in Fig. 10.

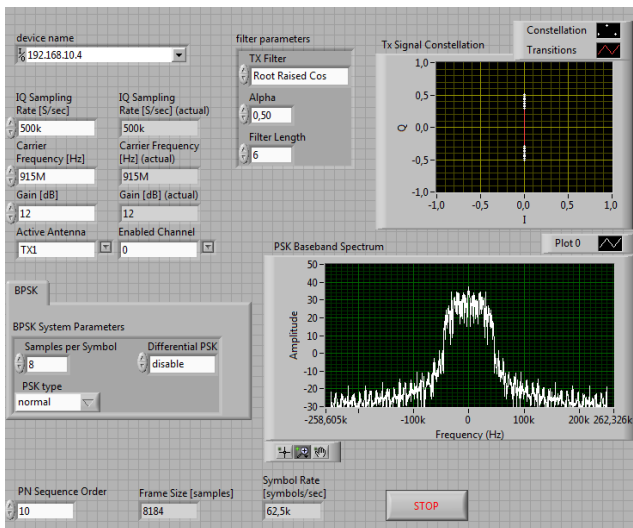


Figure 10. Front Panel of PSK Transmitter Application.

4.2. PSK Implementation with DATEx

Switching halves of the PSK signal's envelopes have the same shape as the message. So PSK generation and the recovery of the data can be handled by conventional Double Side Band Suppressed Carrier (DSBSC) modulation and demodulation techniques. The Sequence Generator module is used for modelling the digital data and its SYNC output triggering the scope to provide a stable screen. With the help of Multiplier module, PSK signal generated as shown in Fig. 11.

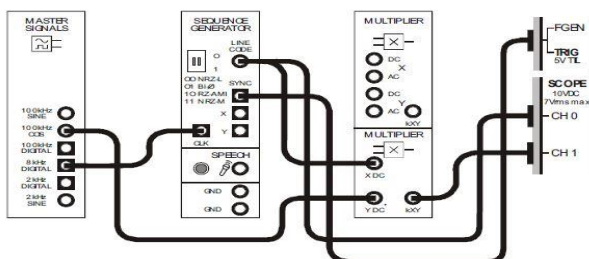


Figure 11. PSK Modulation Scheme

By using second Multiplier and Tuneable LPF module we can easily carry out demodulation part of PSK signal.

5. Conclusions

In this study we propose a new dimension for teaching communication techniques and algorithms by focusing on two computer hosted devices, called USRP and DATEx. ASK, FSK and PSK modulation/demodulation processes presented experimentally. Our approach allows students to concentrate their efforts on real world signals by hands-on applications. Using these devices students will be able to observe signal waveforms, study the effect of noise, design components of a digital communication system. In other words replacing USRP and DATEx with the traditional hardware-only platforms will give students a chance to link the theory with practical implementations.

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